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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	10/661,709
				Filing Date	09/12/2003
				First Named Inventor	Blauvelt et al.
				Art Unit	2878
				Examiner Name	Suezu Y Ellis
				Attorney Docket Number	XPNT22NP
Sheet	1	of	3		

[illegible][illegible]

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	10/661,709
				Filing Date	09/12/2003
				First Named Inventor	Blauvelt et al.
				Art Unit	2878
				Examiner Name	Suezu Y Ellis
Sheet	2	of	3	Attorney Docket Number	XPNT22NP

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>	
		BAZYLENKO ET AL, Fabrication of Light-Turning Mirrors in Buried-Channel silica Waveguides for Monolithic and Hybrid Integration, Journal of Lightwave Technology, 01/1997, Page(s) 148-153, Volume 15, Number 1		
		BOUADMA ET AL, 1.3-um GaInAsP/InP Buried-Ridge-Structure Laser and its Monolithic Integration with Photodetector Using RI Beam Etching, Journal of Lightwave Technology, 05/1994, Page(s) 742-748, Volume 12, Number 5		
		CHAO ET AL, Fresnel Analysis of Effective Mirror Reflectivity in Folded-Cavity In-Plane Surface-Emitting Lasers, IEEE Photonics Technology Letters, 04/1993, Page(s) 390-392, Volume 4, Number 4		
		FUKANO ET AL, A Low-Cost Edge-Illuminated Refracting-Facet Photodiode Module with Large Bandwidth and High Responsivity, Journal of Lightwave Technology, 01/2000, Page(s) 79-83, Volume 18, Number 1		
		FUKANO ET AL, High-Responsivity and Low-Operation-Voltage Edge-Illuminated Refracting-Facet Photodiodes with Large Alignment Tolerance, Journal of Lightwave Technology, 05/1997, Page(s) 894-899, Volume 15, Number 5		
		FUKANO ET AL, Edge-illuminated refracting-facet photodiode with high responsivity and low-operation voltage, Electronics Letters, 12/05/1996, Page(s) 2346-2348, Volume 32, Number 25		
		GFELLER ET AL, 50 mW CW-Operated Single-Mode Surface-Emitting AlGaAs Lasers with 45 deg Total Reflection Mirrors, IEEE Photonics Technology Letters, 07/1992, Page(s) 698-700, Volume 4, Number 7		
		HILLERINGMANN ET AL, Optoelectronic System Integration on Silicon: Waveguides, Photodetectors, and VLSI CMOS Circuits on One Chip, IEEE Transactions on Electron Devices, 05/1995, Page(s) 841-846, Volume 42, Number 5		
		JONES ET AL, Hybrid integration onto silicon motherboards with planar silica waveguides, IEE Proc. Optoelectron., 10/1996, Page(s) 316-321, Volume 143, Number 5		
		KATO ET AL, Large Coupling Tolerance Side-Illuminated Mirror Photodiode for Low-Cost Surface Hybrid Integration, IEEE Photonics Technology Letters, 06/1999, Page(s) 709-711, Volume 11, Number 6		
		STRANDMAN ET AL, Fabrication of 45 deg Mirrors Together with Well-Defined V-Grooved Using Wet Anisotropic Etching of Silicon, Journal of Microelectromechanical Systems, 12/1995, Page(s) 213-219, Volume 4, Number 4		

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